

明德扬科技教育有限公司

异步时序练习 2 资料

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时序参数如下表所示（此表可下载，详见课程简介）

Symbal	描述	Min (ns)	Max(ns)
Tcas	Address_chip select setup time	10	-
Tcah	Address_chip select hold time	10	-
Tcrh	Read high width	200	-
Tcdrh	Read data hold time after read de-assertion	30	120
Tcdch	Chip select assertion to ready high level	0	60
Tcdrd	Read data driving delay	80	200
Tcrr	Ready assertion to read de-assertion	10	-
Trdrh	Read de-assertion to ready de-assertion	10	80
Tcdrt	Chip select de-assertion to ready tri-state	0	100
Tcwh	Write high width	200	-
Tcdwd	Write assertion to ready assertion	80	200
Tcrw	Ready assertion to write de-assertion	10	-
Tcdrs		10	
Twdrh	Write de-assertion to ready de-assertion	0	80